



PATENT
5681-04700/P6708

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/041,024
Filed: January 7, 2002
Inventors:
Morrison, et al.

Examiner: Le, Dieu Minh T
Group/Art Unit: 2114
Atty. Dkt. No: 5681-04700

Title: DATA INTEGRITY DEVICE
FOR PROVIDING
HEIGHTENED ERROR
PROTECTION IN A DATA
PROCESSING SYSTEM

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Name of Registered Representative


Signature

June 29, 2005
Date

INTERVIEW SUMMARY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In the Examiner's Interview Summary mailed with the Notice of Allowance, the Examiner states that "the focus of the present invention is the data integrity device couple[d] to the interconnect and having a plurality of processors configured to be individually enabled or disabled by the host computer system." The Examiner's statement is not accurate for all of the claims. For example, independent claims 1, 62, 66 and 67 do not *require* a *plurality* of processors. Also, independent claims 1, 40, 62, 66 and 67 do not *require individual* enablement/disablement of processors *by a host computer system*. Other implications of the Examiner's statement may also be inaccurate when compared to the actual limitations in the claims. During the telephone interview of April 8, 2005, the differences between the various independent claims were discussed, as

well as how the claims distinguish over the cited art based on each independent claim's particular limitations.

Also enclosed herewith are the following items:

- ☒ Return Receipt Postcard
- ☒ Issue Fee Transmittal form

Respectfully submitted,



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Date: June 29, 2005